

Fig. 1

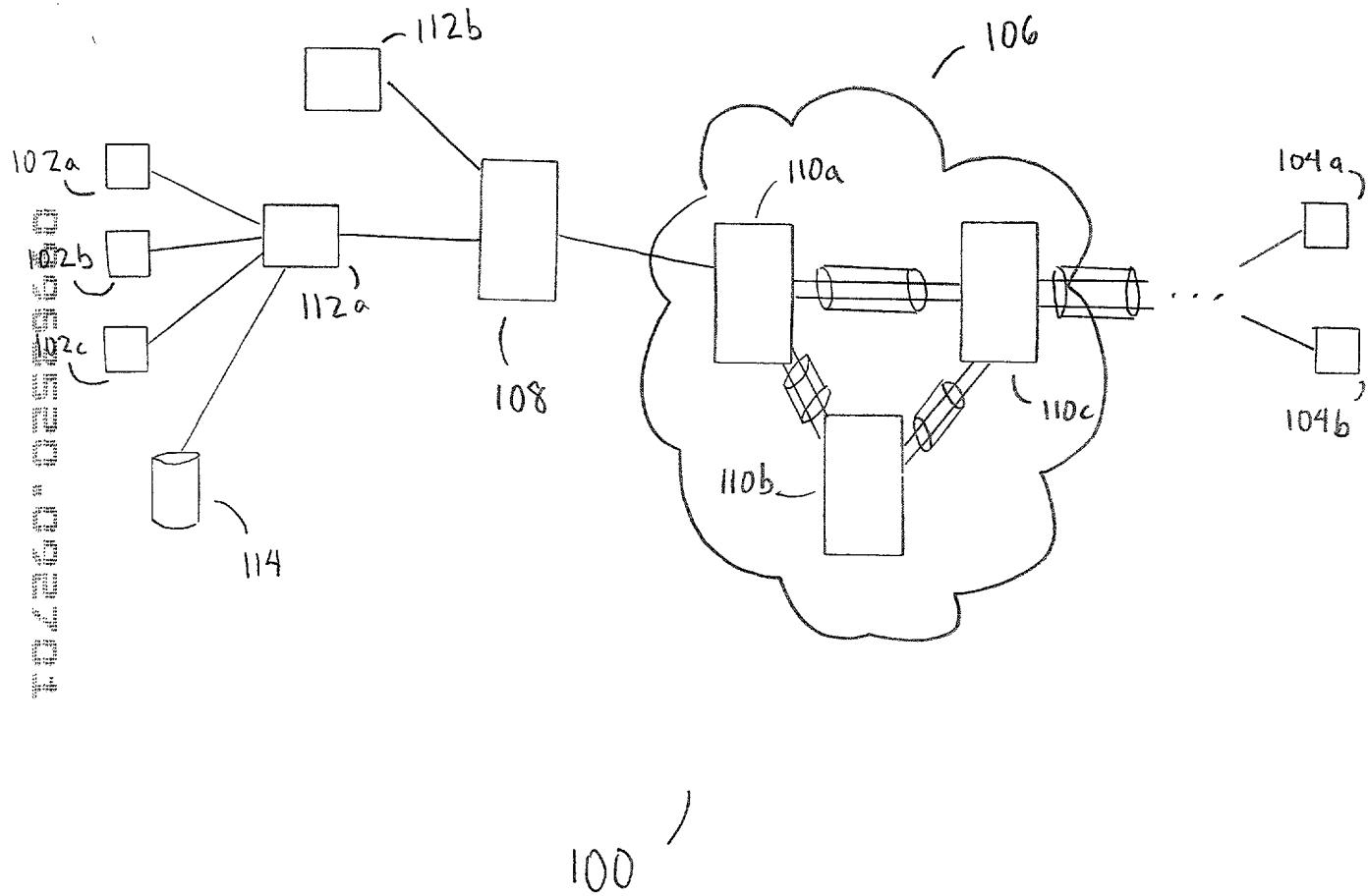


Fig. 2a

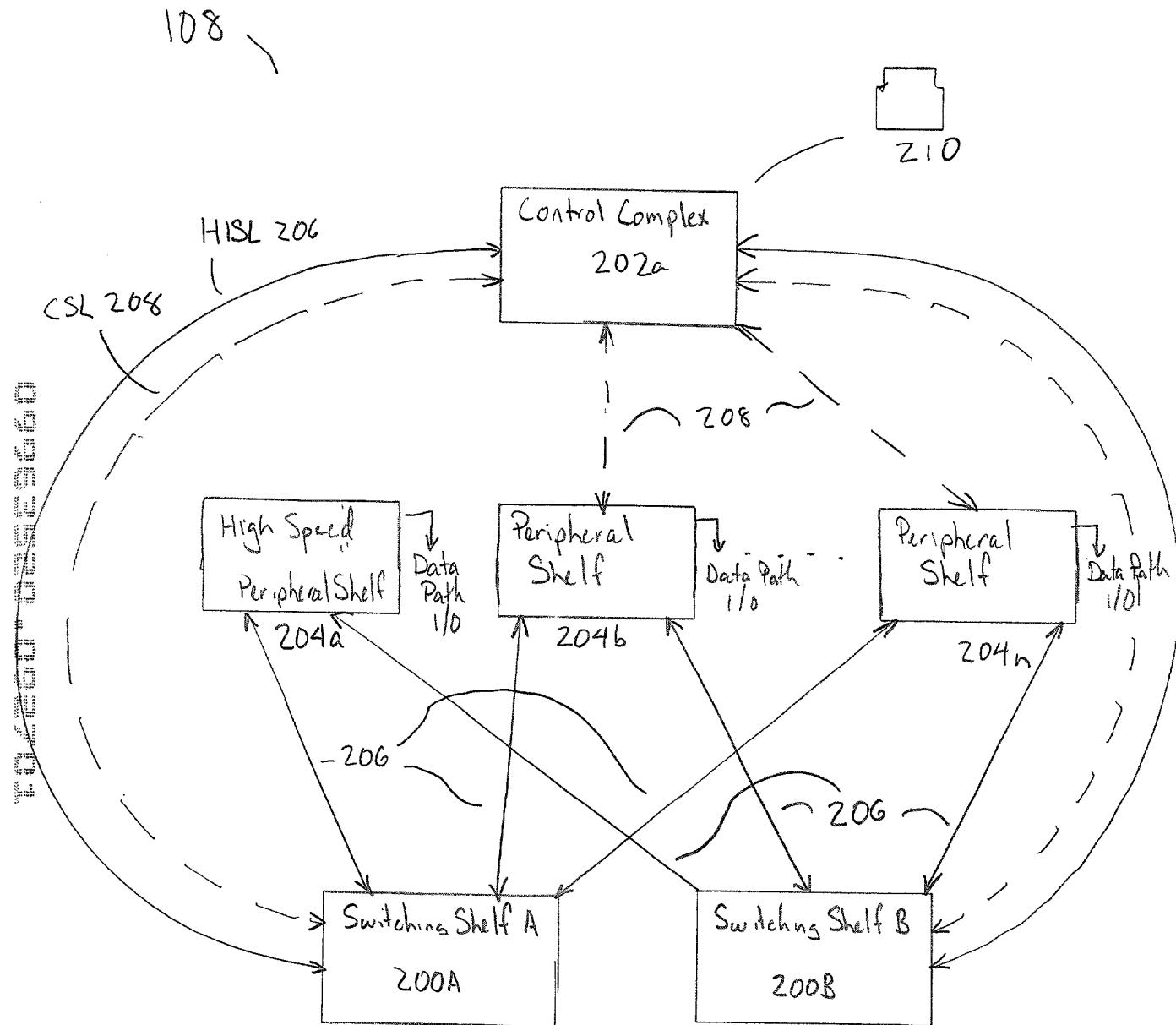


Fig 2B

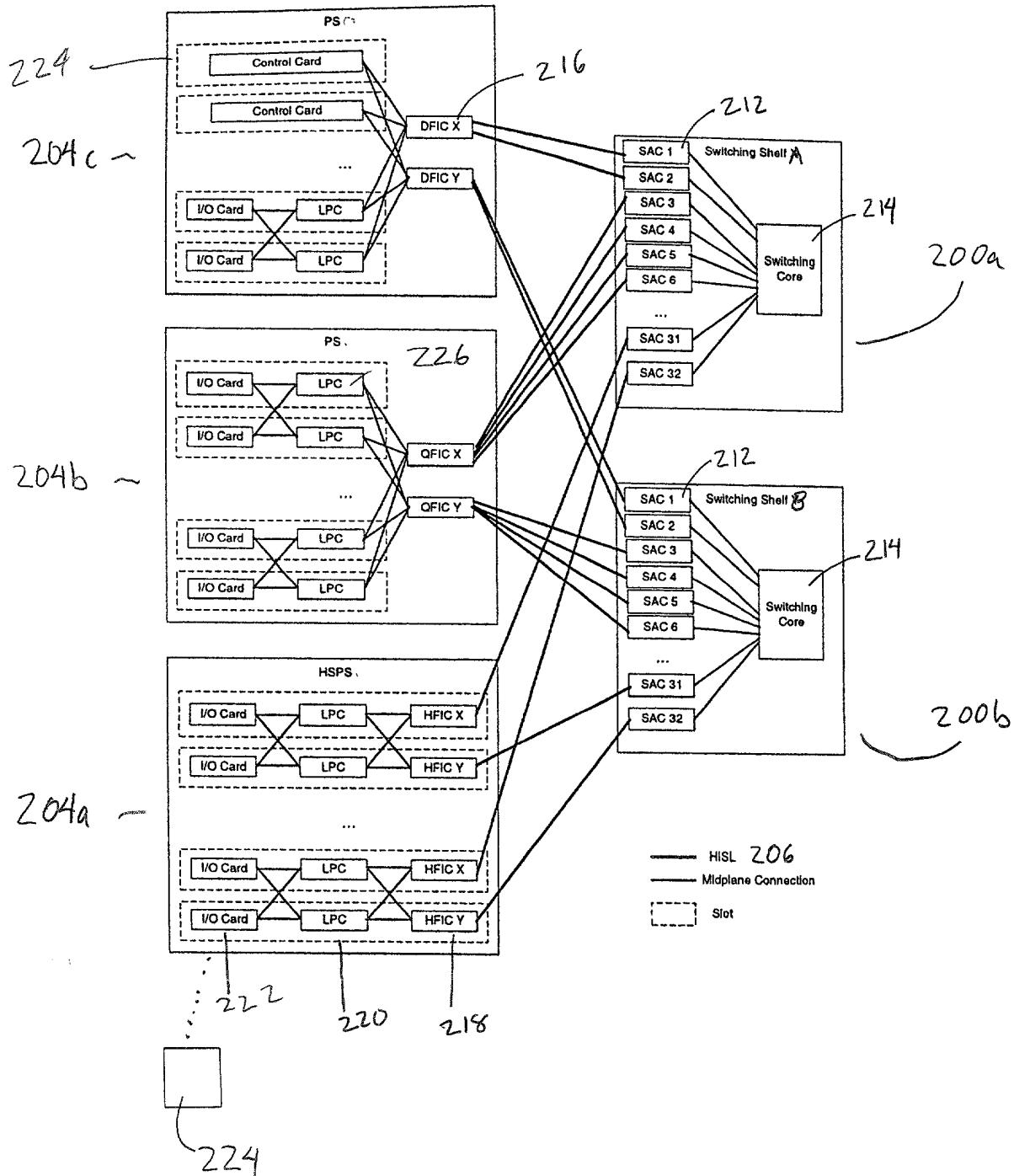


Fig. 2C

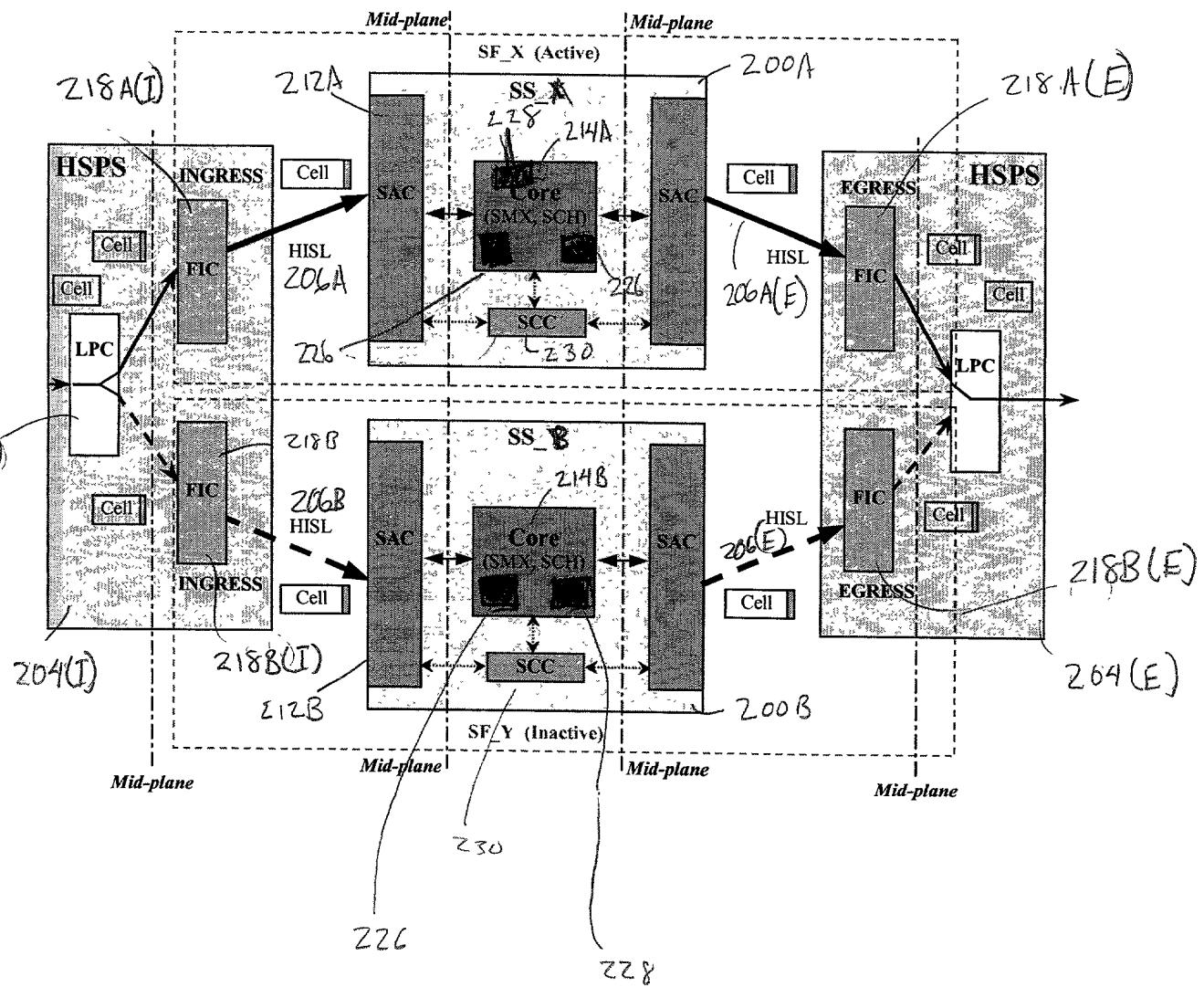


Fig 3

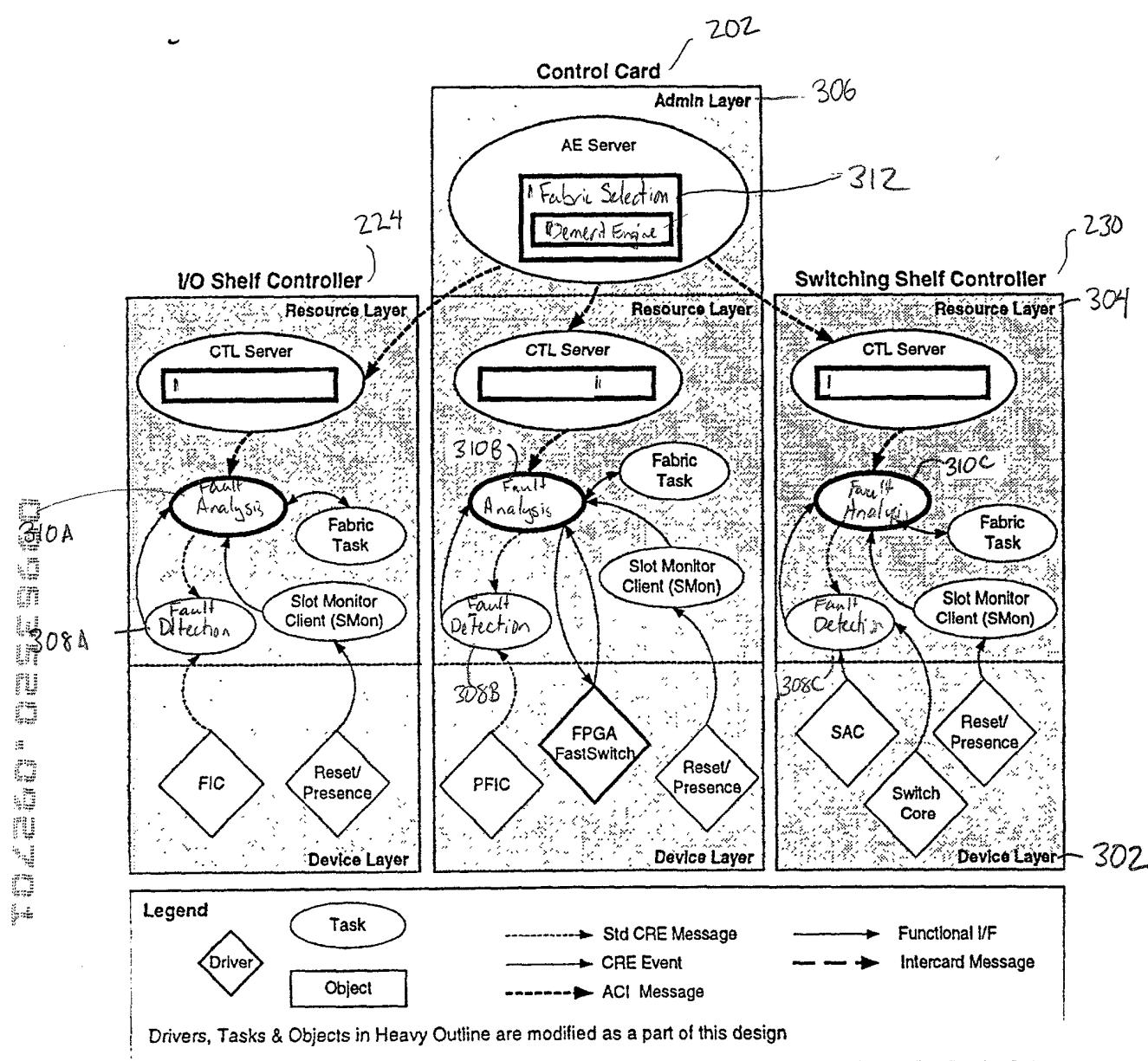


Fig 4

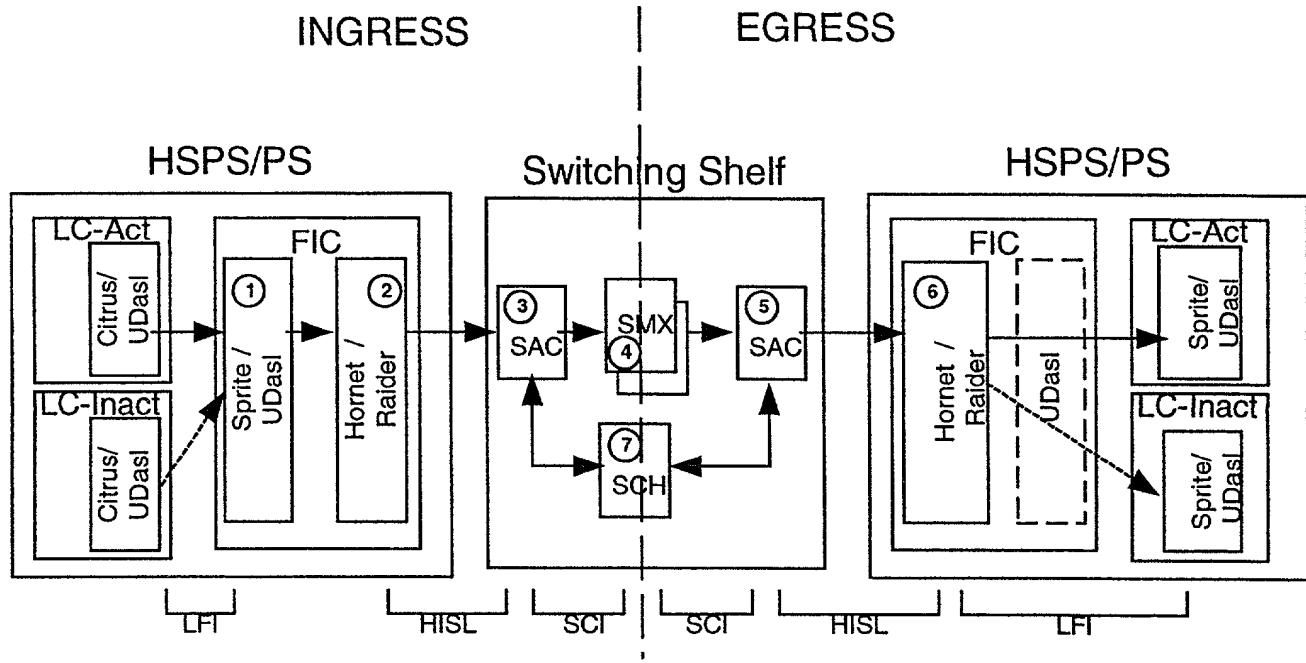


Fig 5

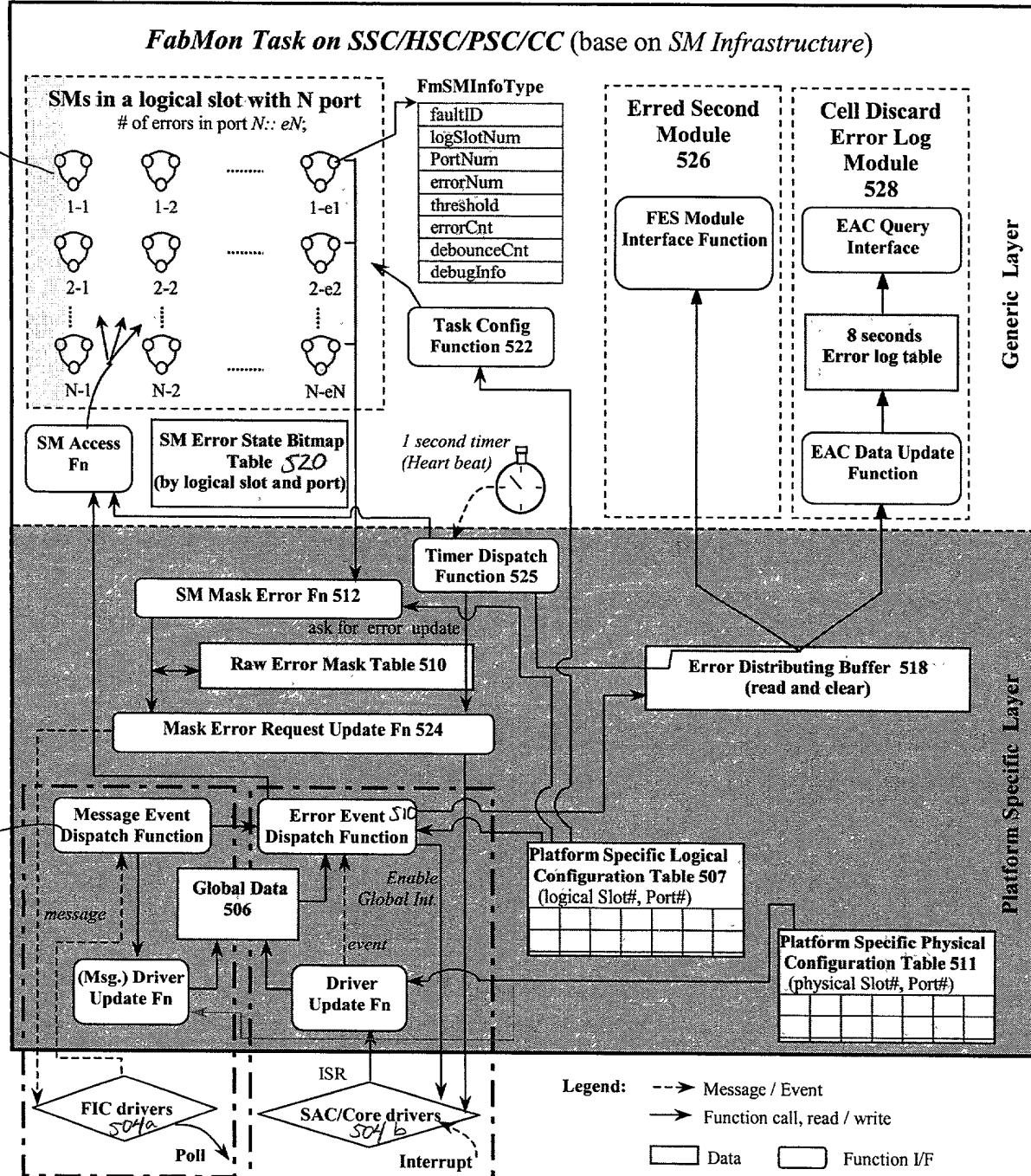
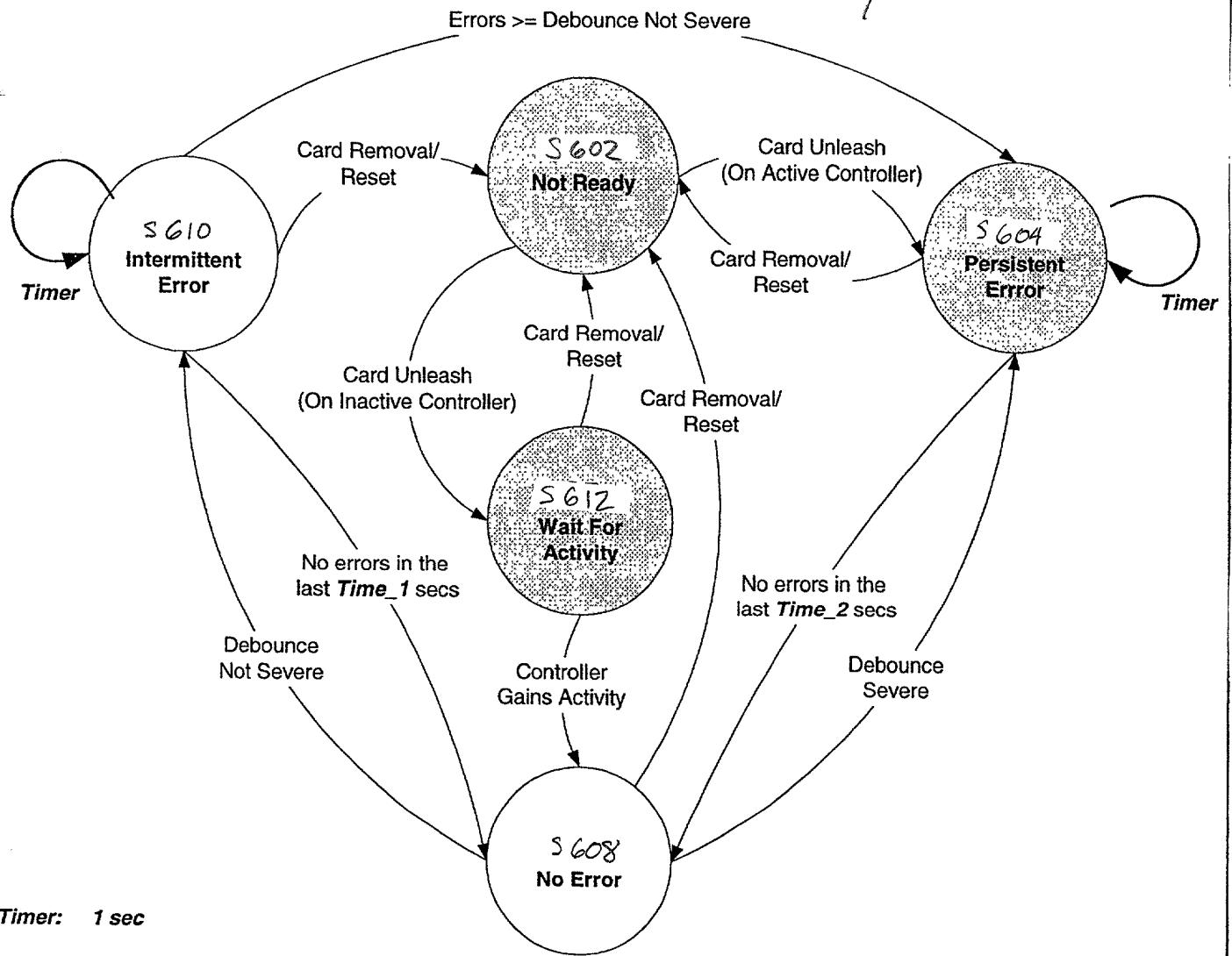


Fig 6A

S09



Timer: 1 sec

Time_1: 2 sec - Debounce time for an intermittent error.

Time_2: 20 sec - Debounce time for a persistent error.

Debounce Severe: 1 Error to go to persistent error.

Debounce Not Severe: 2 Errors to go to persistent error.

(○) State where interrupts are enabled

(●) State where interrupts are disabled

Fig 6B. Physical and Logical Error Tables

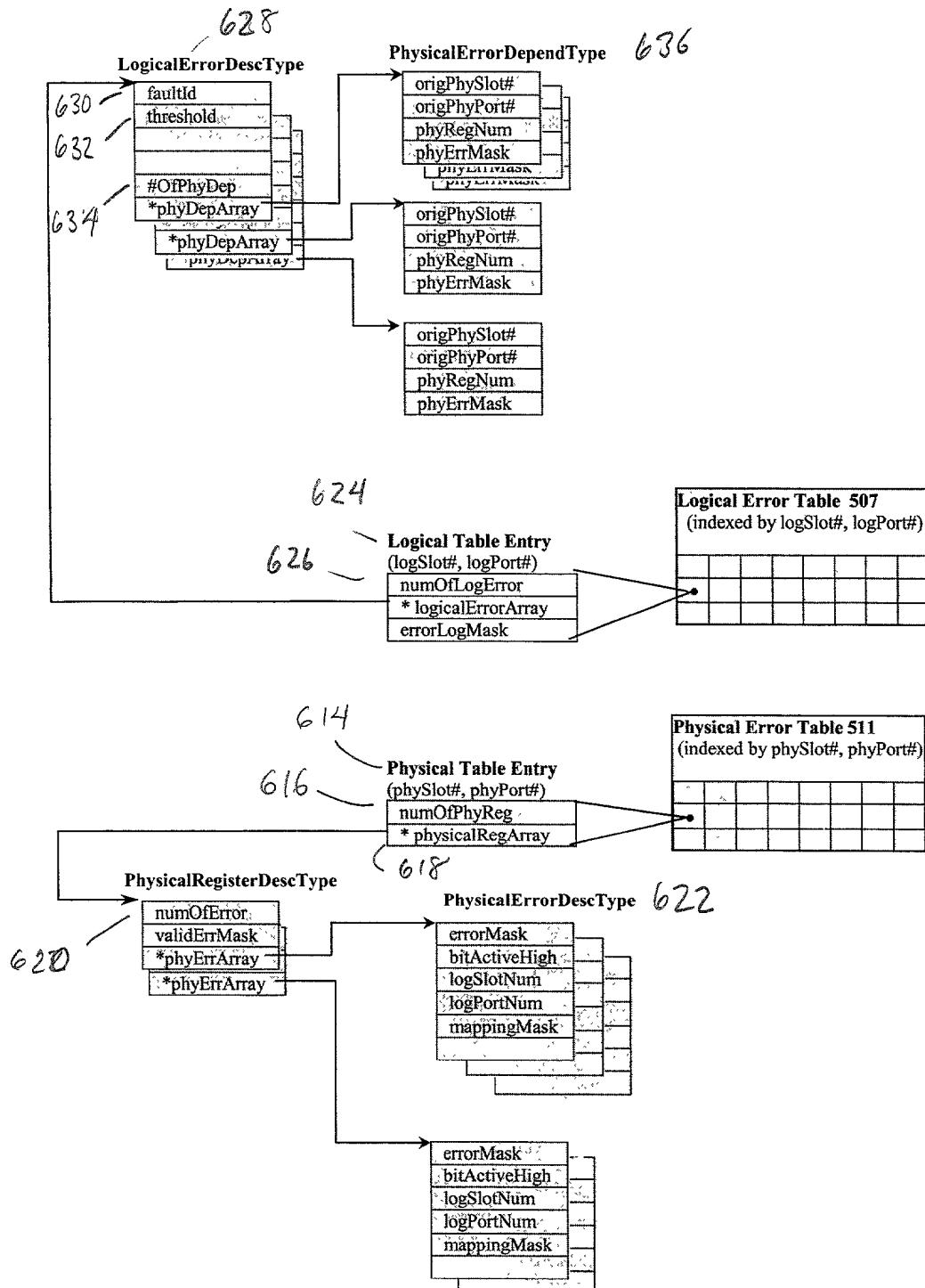
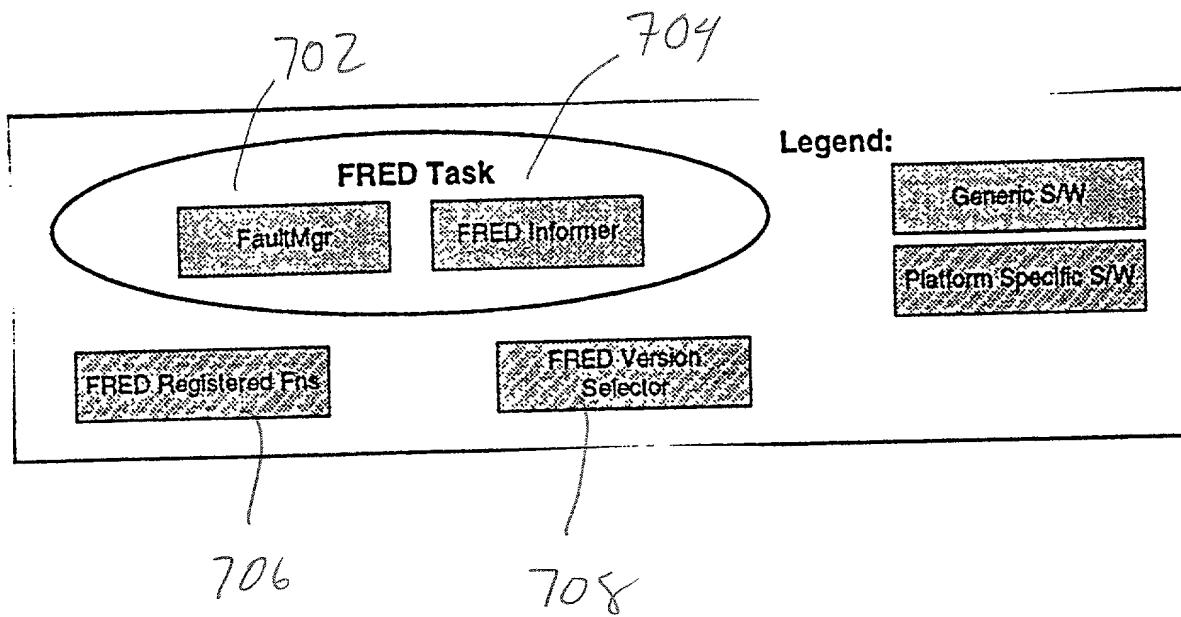


Fig 7



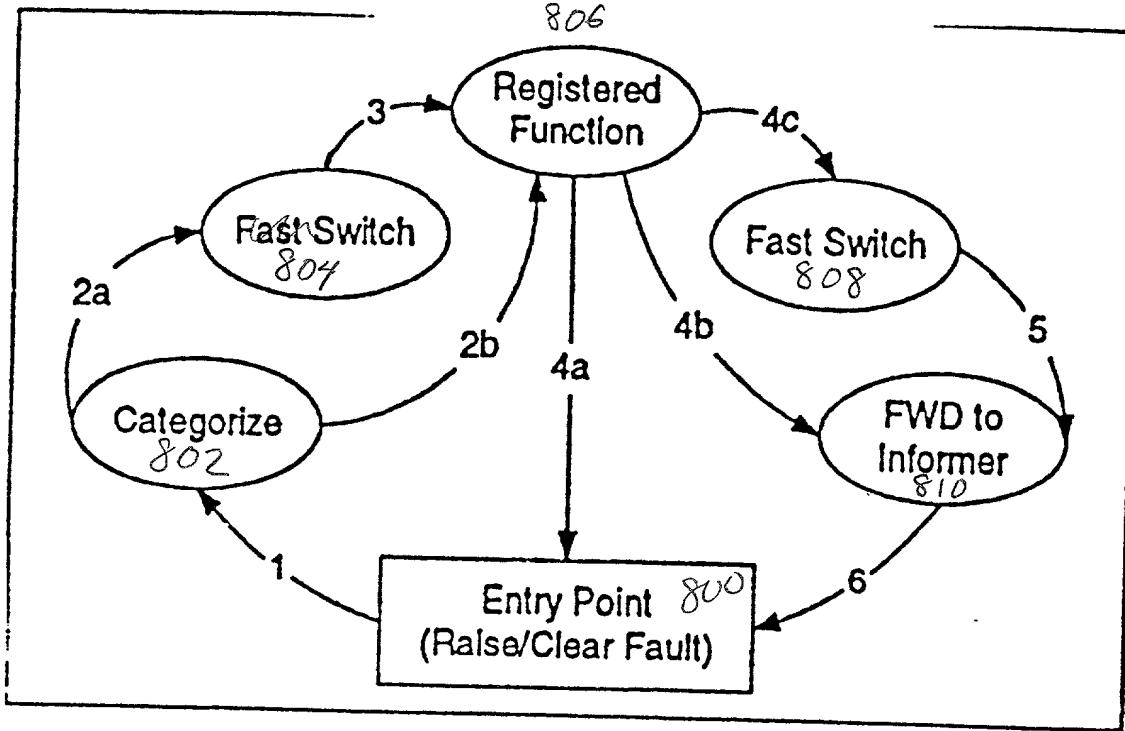


Fig 8

Figure 9a

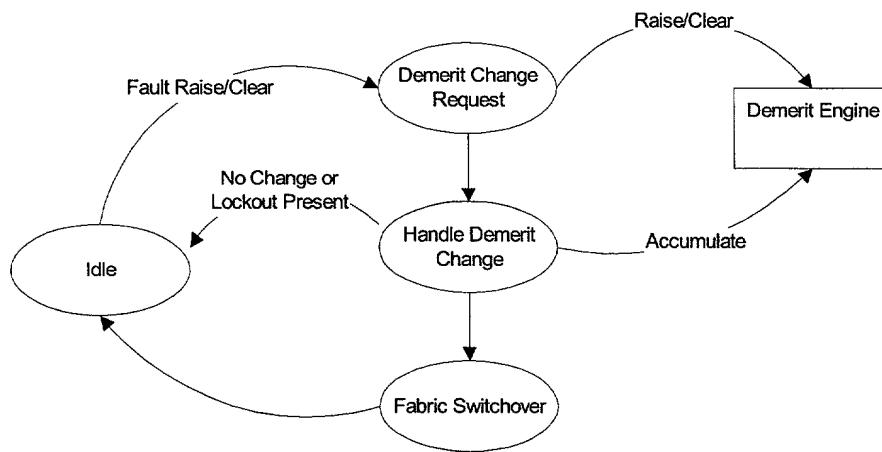


Figure 9b

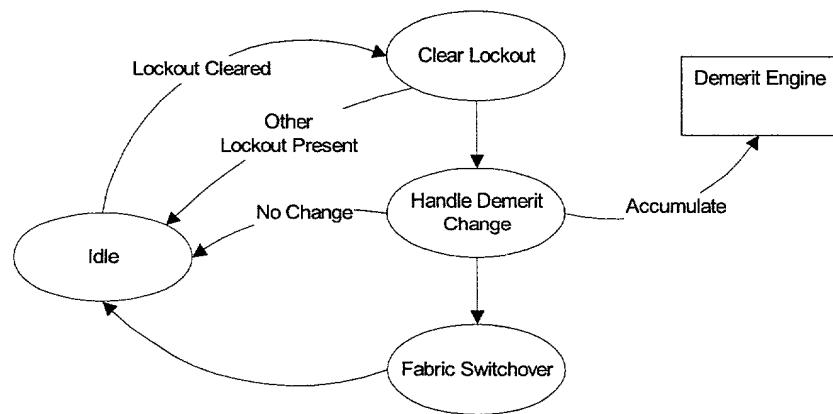


Figure 9c

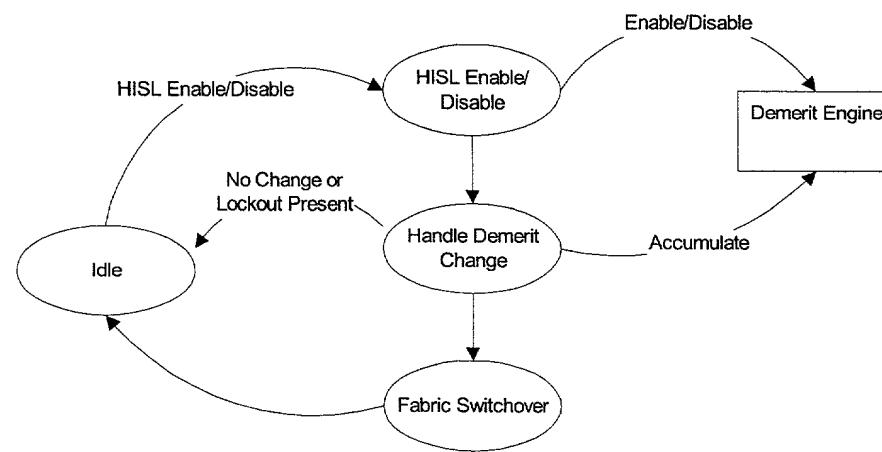


Figure 9d

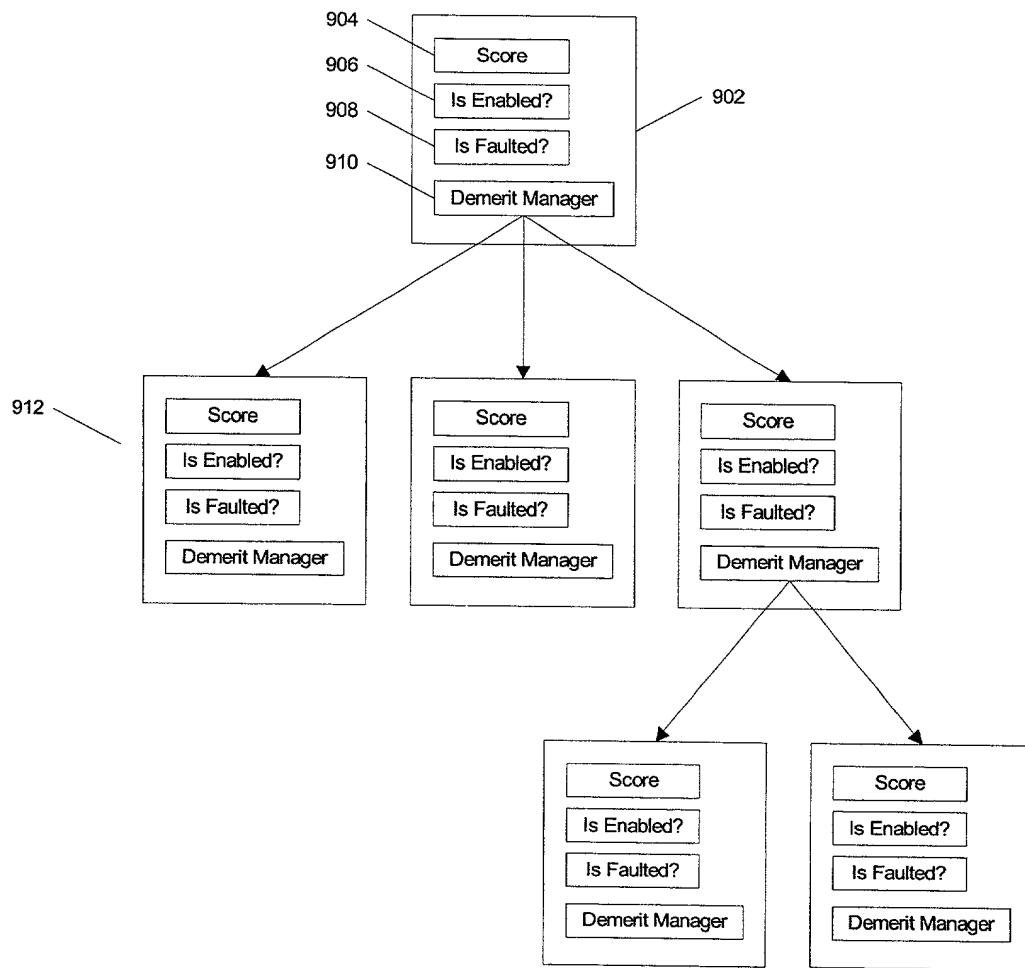


Figure 10

